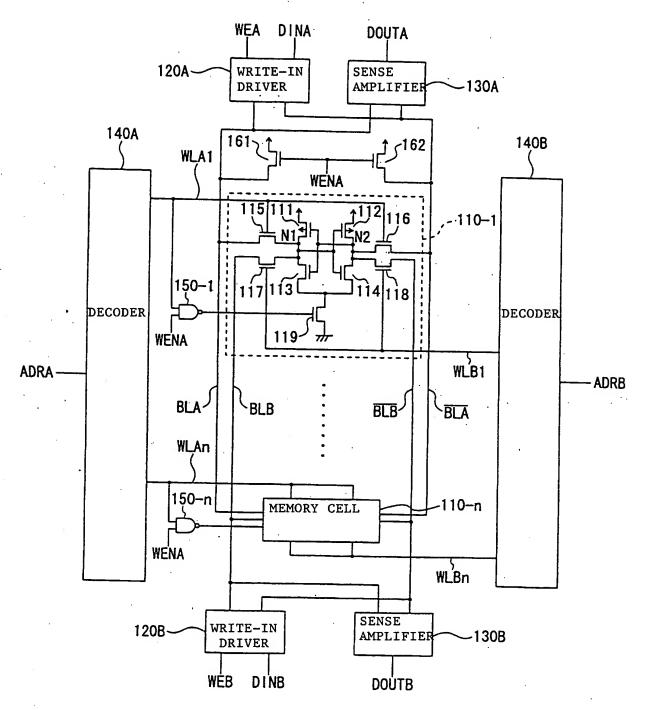
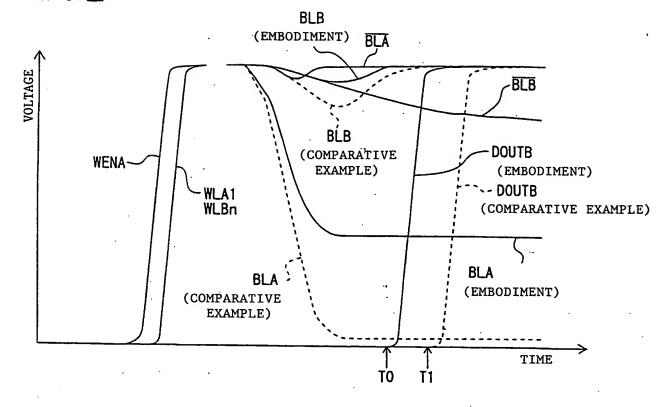
FIG.1

100



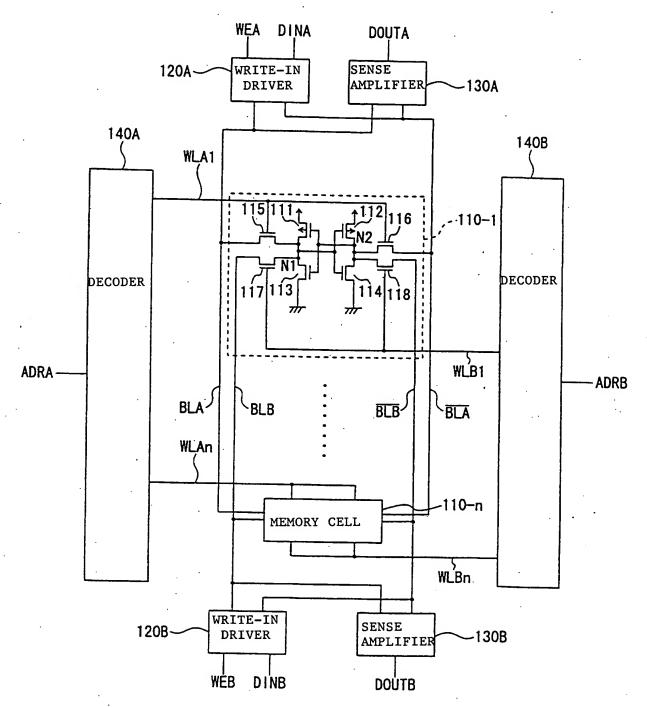
STRUCTURE OF THE FIRST EMBODIMENT

FIG.2



OPERATION OF THE FIRST EMBODIMENT

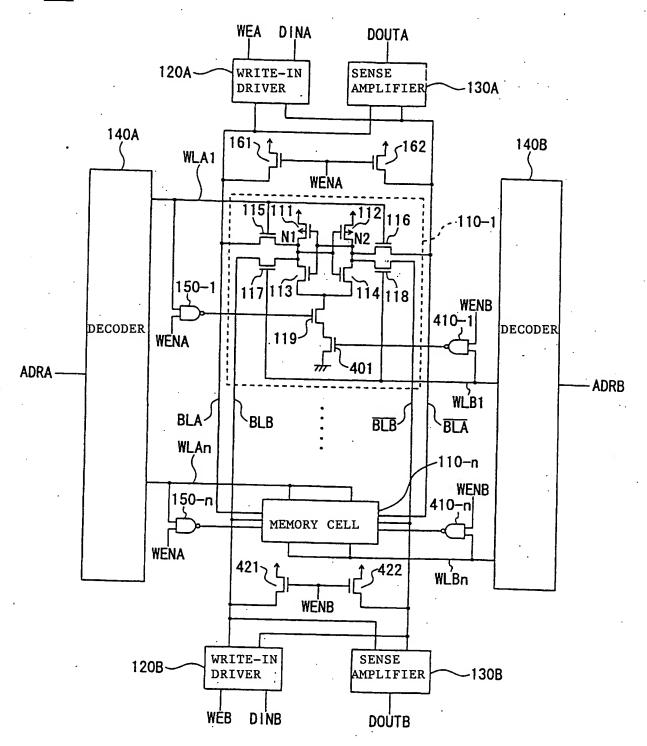
F I G . 3



STRUCTURE OF MULTI-PORT SEMICONDUCTOR MEMORY FOR COMPARISON

FIG.4

400



STRUCTURE OF THE SECOND EMBODIMENT